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OMBINED DECLARATION AND POWER OF ATTORNEY
IN ORIGINAL APPLICATION

As a ~~below~~ named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

CIRCUIT ARRANGEMENT TO DETERMINE THE CURRENT IN A LOAD TRANSISTOR

described and claimed in the specification bearing that title, that I understand the content of the specification, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve month prior to this application, that I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application under 37 C.F.R. 1.56a, and that no application for patent or inventor's certificate of this invention has been filed earlier than the following in any country foreign to the United States prior to this application by me or my legal representatives or assigns:

German Application No. 100 42 585.2, filed August 30, 2000, the International Priority of which is claimed under 35 U.S.C. §119.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

HERBERT L. LERNER (Reg.No.20,435)
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I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

FULL NAME OF SOLE INVENTOR: RAINALD SANDER

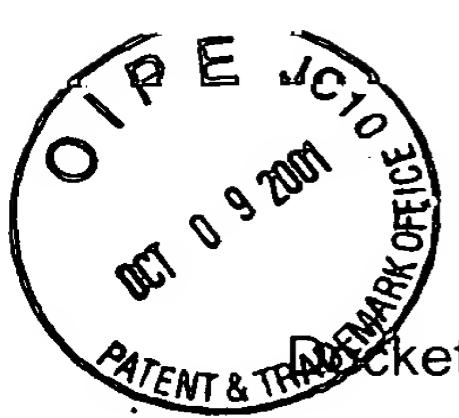
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Power
E. Willis
12-13-01

cket No.: WMP-IFT631

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Rainald Sander
Appl. No. : 09/943,589
Filed : August 30, 2001
Title : Circuit Arrangement to Determine the Current in a Load
Transistor
Art Unit :

ASSOCIATE POWER OF ATTORNEY

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Please recognize GREGORY L. MAYBACK (Reg. No. 40,719) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

For Applicant(s)

LAURENCE A. GREENBERG
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Date: October 3, 2001

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